



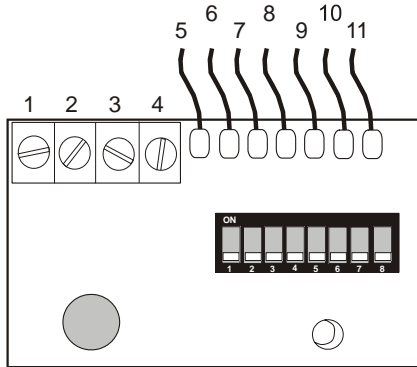
GE Interlogix

ARITECH

# AD111 Internal PID Interface

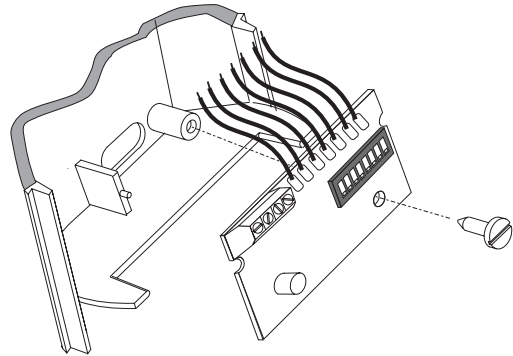


### 1 AD111

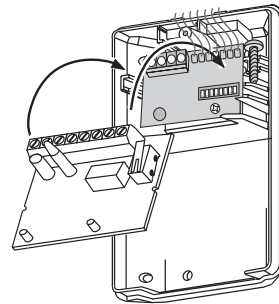


### 2

A

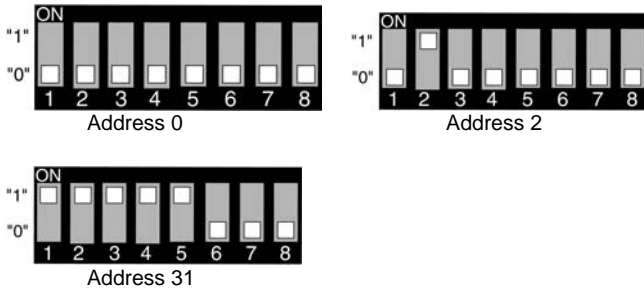


B



EV125 Series  
EV425 Series  
EV500 Series

### 3



("1" = ON)

Address	Setting
0	00000000
1	10000000
2	01000000
3	11000000
4	00100000
5	10100000
6	01100000
7	11100000

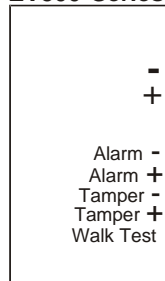
Address	Setting
8	00010000
9	10010000
10	01010000
11	11010000
12	00110000
13	10110000
14	01110000
15	11110000

Address	Setting
16	00001000
17	10001000
18	01001000
19	11001000
20	00101000
21	10101000
22	01101000
23	11101000

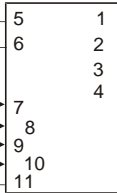
Address	Setting
24	00011000
25	10011000
26	01011000
27	11011000
28	00111000
29	10111000
30	01111000
31	11111000

### 4

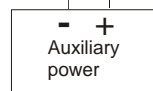
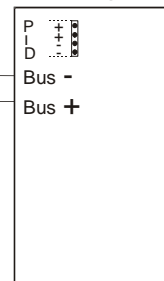
EV125/EV425/  
EV500 Series



AD111



ATS1290 (DGP)



ATS2000/3000/400  
(Control Panel)



## INTRODUCTION

The AD111 is an internal interface for an addressable detector system. It connects a generic detector with dry alarm and tamper contacts to the Advisor MASTER ATS1290 Point-ID DGP. The interface is equipped with two inputs for connecting the alarm relay and the tamper switch of the detector, and an open collector output. This output can be used for example; to enable/disable the alarm LED on the detector (depending on the detector's other functionalities, which can be programmed).

## DEVICE CATEGORY

The AD111 is of the device category I/O, type 2 and will be automatically recognised after learning the device into the ATS1290 DGP. For more details about device categories and types, please refer to the ATS1290 manual.

## MOUNTING THE UNIT

The AD111 PCB (Fig. ①) is mounted underneath the PCB in the housing of the detector that is connected to the Point-ID bus (Fig. ②). Connect terminals 1 to 4 to the Point ID bus and external power supply.



**CAUTION!** Before mounting the AD111 Internal PID interface in a (existing) PIR detector, disconnect the mains power before opening the panel, DGP or external power supply cabinet.

1. Open the detector.
2. Remove the PCB from the detector.
3. Connect the flying leads 5 - 11 to the existing detector connections (see fig. ①, ② and ④).

Number	Colour	AD111	PIR
5	Black	0V from AD111	0V to PIR
6	Red	+12V from AD111	+12V to PIR
7	Blue	Alarm input	Alarm output
8	Blue	Alarm input	Alarm output
9	Yellow	Tamper input	Tamper output
10	Yellow	Tamper input	Tamper output
11	White	Walk test output	Walk test input

4. Connect the Point ID bus and auxiliary power supply to terminals 1 - 4 (see Fig. ④).

### Important:

**The Point ID bus in – (ground) must be coupled to the external power supply in – (ground) to ensure correct operation! Connect the bus and detector grounds in the control panel or close to the detector where the auxiliary power supply is located.**

## TOPOLOGY AND DETECTORS

Please refer to the ATS1290 manual.

## CABLE SPECIFICATION

Please refer to the ATS1290 manual.

## ADDRESS SETTING AND ZONE NUMBERING

Set the AD111 Internal PID Interface to its unique address on the Point ID bus. For details, see figure ⑤.

The address setting of the AD111 will immediately correspond with zone number and output in the range of the ATS1290 DGP

Refer also to the ATS1290 manual for the direct link to the zone number (control panel).

For convenience the output also can be configured to connect to any output in the DGP range (for more details see the explanation at the end of the memory location map).

## THE ATS OUTPUT

The walk test can be programmed through the control panel by activating an output in the DGP range.

## MEMORY LOCATION MAP

Every PID device has its own characteristics concerning related I/O lines or certain functionality of the device.

A total of 16 memory locations are reserved to configure the PID devices. Only the first nine normally are user configurable. This depends on the functionality of the particular device.

For the AD111 internal PID interface only memory location 6 matters.

On memory location 6 the ATS-style output can be configured.

### \*ATS-style output

The output can be used to enable/disable the Alarm LED or Walk Test LED on the detector. ATS output and zone numbering are equivalent.

The default output number of the device equals the input number when memory location 6 is set to 0 (default).

Please refer to the PID DGP manual for PID address settings and zone numbering.

The output number on the AD111 can also be changed within the DGP output range (e.g. DGP 1 output 17-32). If the output of this device or all the outputs of similar devices on the same DGP need to be programmed on for example output 32, simply program memory location 6 of all those I/O devices to 32.

## TECHNICAL SPECIFICATIONS

Bus protocol	GE Interlogix Point ID
Bus voltage	12 or 24 V $\pm$
External power supply	9 – 13.8 V $\pm$
Current consumption from bus	330 $\mu$ A $\pm$ 10%
Unit load for DGP	1
Address range	0 to 255
Current O/C output	100 $\mu$ A max.
Category	I/O
Device type	T2

## MEMORY LOCATION MAP FOR AD111

Location	Function	Decimal values	Binary values
6	ATS-style output*	0	0000 0000 ATS output number that controls the AD011/AD111 output by means of "output on" / "output off" *