



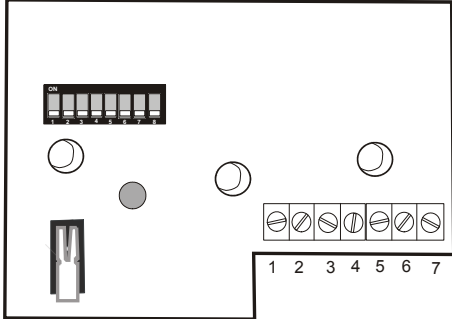
GE Interlogix

ARITECH

AD011 External PID Interface



1 AD011



2



("1" = ON)

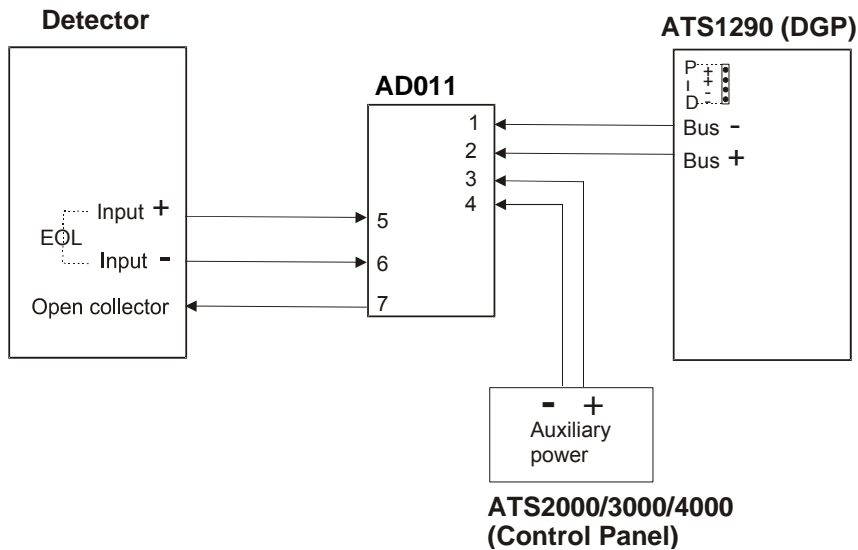
Address	Setting
0	00000000
1	10000000
2	01000000
3	11000000
4	00100000
5	10100000
6	01100000
7	11100000

Address	Setting
8	00010000
9	10010000
10	01010000
11	11010000
12	00110000
13	10110000
14	01110000
15	11110000

Address	Setting
16	00001000
17	10001000
18	01001000
19	11001000
20	00101000
21	10101000
22	01101000
23	11101000

Address	Setting
24	00011000
25	10011000
26	01011000
27	11011000
28	00111000
29	10111000
30	01111000
31	11111000

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AD011 External PID Interface

INTRODUCTION

The AD011 (see fig. 1) is designed as a universal external interface for an addressable detector system. It connects a generic detector with a dual loop (2-resistor configuration) connection to the Advisor MASTER ATS1290 PID DGP. The interface is equipped with one balanced input to connect the alarm / tamper output of the detector and an open collector output. This output can be used, for example, to enable / disable the alarm LED on the detector (depending on the detector's other functionalities, which can be programmed).

DEVICE CATEGORY

The AD011 is of the device category I/O, type 3 and will be automatically recognised after learning the device into the ATS1290 DGP. For more details about device categories and types please refer to the ATS1290 manual.



MOUNTING THE UNIT

Due to the free topology of the PID system, the AD011 can be mounted some distance from the Point ID bus and detector.

1. Open the housing of the AD011.
2. Remove the PCB from the unit.
3. Position the unit where you want it mounted, e.g. on the ceiling or wall.
4. Drill the necessary holes for fixing the housing.
5. Fix the housing to the ceiling or wall.
6. Reinstall the PCB.

CONNECTING THE UNIT

Connect the AD011 to the detector and Point ID bus (see fig. 3)

1. Decide for a double or single resistor EOL configuration
2. Connect the detector alarm/tamper outputs via the selected EOL resistor(s) to the input connectors 5 and 6.
3. Connect the external power supply to connectors 3 and 4.
4. Connect the alarm LED enable connection from the detector to the open collector output 7 (if applicable).
5. Connect the Point ID bus to connectors 1 and 2.

Important:

The Point ID bus in – (ground) must be coupled to the external power supply in – (ground) to ensure correct operation!

ADDRESS SETTING, ZONE NUMBERS AND OUTPUT NUMBERS

Set the AD011 to its unique address on the Point ID bus. For details, see figure 2.

MEMORY LOCATION MAP FOR AD011

Location	Function	Decimal values	Binary values
3	Zone types*	0 = Zone is off 1 = Single EOL 2 = Double EOL (default)	0000 0000 = Zone is off 0000 0001 = Single EOL 0000 0010 = Double EOL (default)
6	ATS-style output**	0	0000 0000 ATS output number that controls the AD011/AD111 output by means of "output on" / "output off" *

***Zone Types**

The ATS1290 default value for input type is Double EOL (2). Consequently the normal state is a single EOL resistor, for an alarm state it is two EOL resistors in series, and for an input tamper it can be either "Short" or "Open".

****ATS-style output**

The output can be used to enable/disable the Alarm LED or Walk Test LED on the detector. ATS output and zone numbering are equivalent.

The default output number of the device equals the input number when memory location 6 is set to 0 (default). Please refer to the PID DGP manual for PID address settings and zone numbering.

The output number on the AD011 can also be changed within the DGP output range (e.g. DGP 1 output 17-32). If the output of this device or all the outputs of similar devices on the same DGP need to be programmed on for example output 32, simply program memory location 6 of all those I/O devices to 32.

The address setting of the AD011 will immediately correspond with zone number and output in the range of the ATS1290 DGP.

Refer also to the ATS1290 manual for the direct link to the zone number (control panel).

For convenience the output also can be configured to connect to any output in the DGP range (for more details see the explanation at the end of the memory location map).

THE ATS OUTPUT

The walk test, for example, can be programmed through the control panel by activating an output in the DGP range.

MEMORY LOCATION MAP

Every PID device has its own characteristics concerning related I/O lines or certain functionality of the device.

A total of 16 memory locations are reserved to configure the PID devices. Only the first nine normally are user configurable. This depends on the functionality of the particular device.

For the AD011 external PID interface only memory locations 3 and 6 matter.

On memory location 3 the zone type can be configured for Single EOL, Double EOL (default) or the zone can be switched off (the status will always be "Normal").

The ATS-style output can be configured on memory location 6. For more details, please refer to the explanation at the end of the Memory Location map.

TECHNICAL SPECIFICATIONS

Bus protocol	GE Interlogix Point ID
Bus voltage	12 or 24 V $\overline{=}$
External power supply	9 to 13.8 V $\overline{=}$
Current consumption from bus	520 μ A \pm 10%
Unit load for DGP	2
Address range	0 to 255
Current O/C output	100 μ A max.
EOL resistors	2 x 4.7 k Ω
Category	I/O
Device type	T3